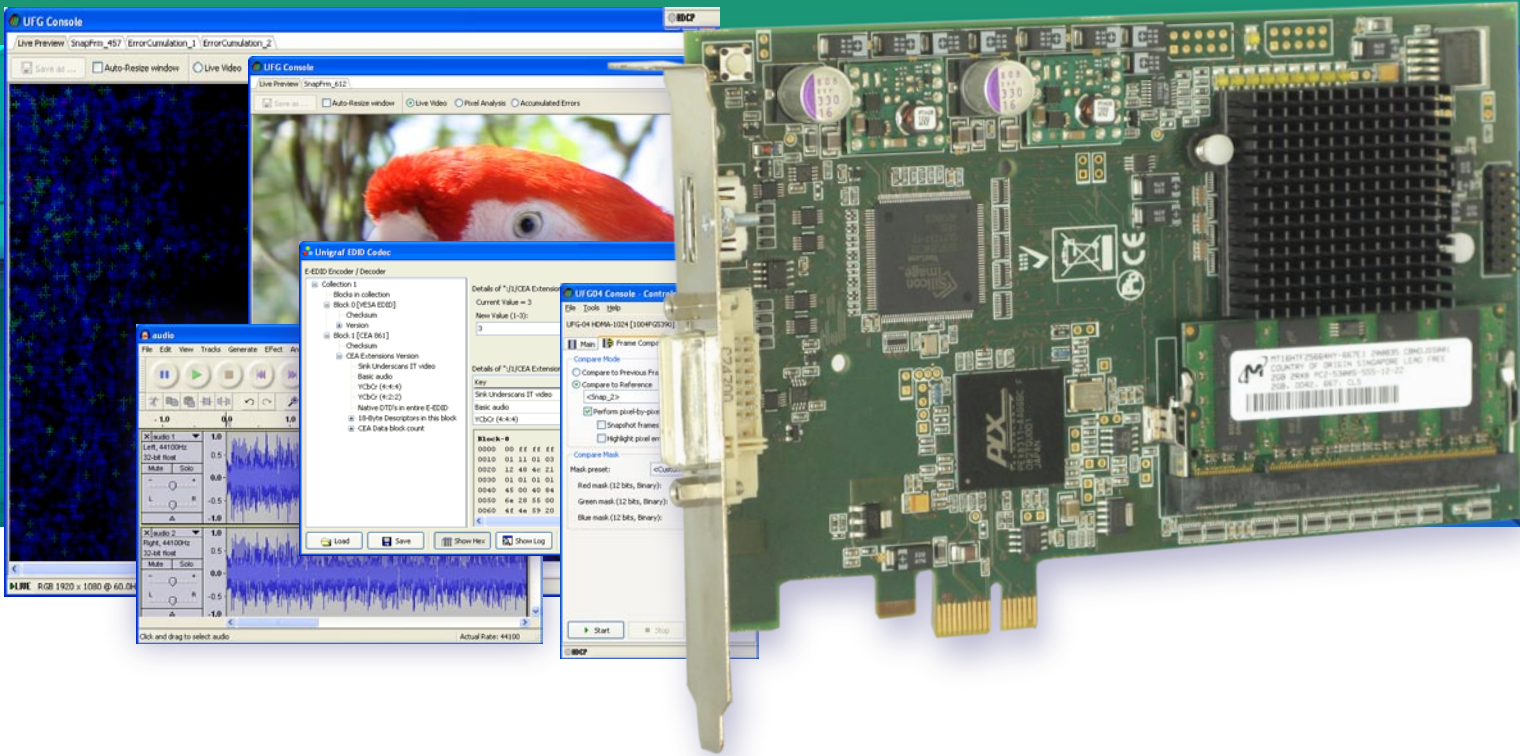


UFG-04 HDMA

HDMI 1.3 frame grabber and stream analyzer



HDMI 1.3 Stream Analyzer

UFG-04 HDMA frame grabber card enables the capture of HDMI 1.3 content with up to 12 bits per color component and resolution up to VESA 1920x1200@60 (RB) and Full HD 1080p60. The on-board frame buffer enables the capture of up to 1000 frame-to-frame video clips with audio regardless of the PC bottlenecks.

Analyze Image and Metadata

Unigraf UFG04 Console software together with UFG-04 HDMA frame grabber card is a unique tool for testing HDMI source devices. You can easily verify the fidelity of the stream frame to frame, pixel to pixel, bit to bit. You can monitor all information on the HDMI stream including video, audio, link status and InfoFrames. Testing the compatibility of your source device is easy and straightforward. The SDK with DLL library and example applications enables the full functionality in custom testing systems.

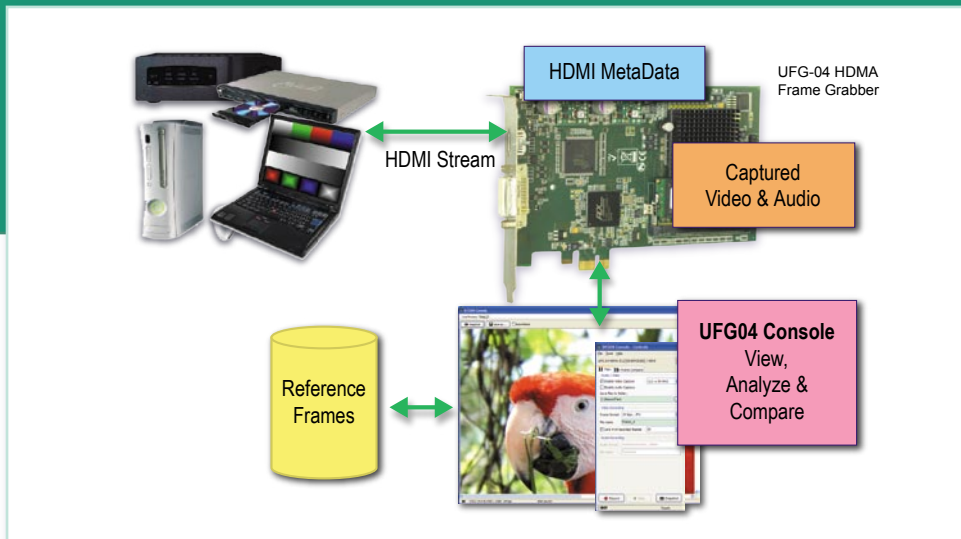
Benefits

- HDMI 1.3 Deep Color capture
- Up to 1920x1200@60 (RB) and 1080p60 Full HD
- Up to 12 bits per color depth
- Up to 1000 frames on-board capture
- User programmable EDID
- HDCP compatible
- SDK with DLL library
- Full HDMI source analysis with UFG04 Console

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05/2010



UFG04 Console

Test the fidelity of your HDMI source within seconds. Measure each of the millions of pixels reliably each time. Analyze any test image and the associated metadata to find any mismatches. Review each individual result in detail and include the long term trends into your quality reports.

With the user programmable EDID you can emulate the problem sink devices and verify the performance of your source reliably. By using UFG04 SDK, you can build automated test sequences that can repeatedly perform your routine compliance tests.

Video Capture without Dropped Frames

The UFG-04 series frame grabbers provide a unique feature of capturing up to 1000 non-compressed frames into the on-board frame buffer. By using this unique feature the user can e.g. evaluate display controller rendering pixel by pixel and without lost frames.

Specifications

Inputs	HDMI Type A and DVI-D connector Silicon Image SiI9135 receiver
Color Spaces	RGB or YCbCr
Capture Pixel Depth	18, 24, 30 or 36 bits per pixel
Resolutions	All VESA DMT/CVT and CEA 861-E timings up to 1080p60 and 1920x1200@60 (RB).
Input Bandwidth	225 MHz maximum TMDS clock
Frame Buffer	2 or 4 GBytes
EDID	Load EDID data from file, Program new EDID, Display & edit EDID contents
Audio	8 channels. Capture audio to a file
Data Interface	PCI Express 1 lane.
Operating Systems	Windows® XP
SW Interface	Custom C/C++ library with full functionality to configure the board and capture video, audio, metadata and link status. Multi-board Support
Module Size	107 x 168 mm
Power Consumption	12 V: 7.5 W max; 3.3 V: 1.7 W max

All specifications subject to change without notice.



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